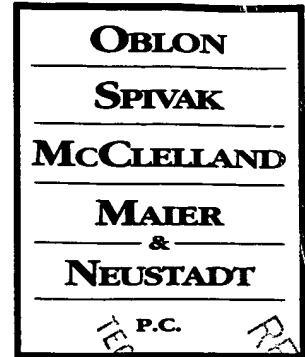




Docket No.: 204612US2

ASSISTANT COMMISSIONER FOR PATENTS
WASHINGTON, D.C. 20231

RE: Application Serial No.: 09/802,886
Applicants: Yuuichi HIRANO, et al.
Filing Date: March 12, 2001
For: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME
Group Art Unit: 2811
Examiner: T. TRAN



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SIR:

Attached hereto for filing are the following papers:

SUPPLEMENTAL ELECTION RESPONSE (Courtesy Copy)

No check is attached since it is believed no fees are owed. In the event fees are owed and the Patent Office charges for filing the above-noted documents, including any fees required under 37 C.F.R 1.136 for any necessary Extension of Time to make the filing of the attached documents timely, please charge or credit the difference to our Deposit Account No. 15-0030. Further, if these papers are not considered timely filed, then a petition is hereby made under 37 C.F.R. 1.136 for the necessary extension of time. A duplicate copy of this sheet is enclosed.

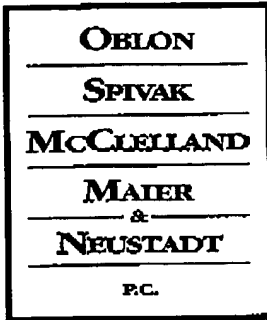
Respectfully submitted,

OBLON, SPIVAK, McCLELLAND,
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